Appl. No. 10/768,394

Amdt. Dated April 29, 2005

Reply to Office Action of February 24, 2008



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Mami KAWABATA et al.

Serial No. 10/768,394

Confirmation No. 3798

Filed:

January 30, 2004

For:

Selective Switching of a Transistor's

Back Gate Potential (as amended)

AMENDMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 Art Unit: 2816

Examiner: Englund, Terry L.

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

April 29, 2005
Date of Deposit
Juanita Soberanis

Name Glanck Loberanis 04/29/2005

[/]Signature

Date

Dear Sir:

In response to the Office Action dated February 24, 2005, please amend this application as follows:

Amendments to the specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Amendments to the Drawings begin on page 9 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 10 of this paper.

An Appendix including amended drawing figures is attached following page 11 of this paper.

05/04/2005 RFEKADU1 00000011 501314 10768394

01 FC:1201

200.00 DA